Docket No.: MR2863-12

COMBINED DECLARATION AND POWER OF ATTORNEY

As a below named inventor, I hereby declare that:

I believe that I and joint inven patent is sough the specificatio (X) is attact () was fil	am an original, first a tor (if plural names a t on the invention end on of which (check or shed hereto. ed on	re listed below) of the subjectitled: Circuit Layout Arran	ne name is listed below) or an original, first ect matter which is claimed and for which a gement for Key Switch Signal Recognition al No.
		ed and understand the cor	ntents of the above-identified specification above (if any).
		information which is mater rederal Regulations, Section	rial to the examination of this application in 1.56.
foreign applica	ation listed below a	and have also identified be	d States Code, Section 119 of the following elow any foreign application for patent of cation on which priority is claimed:
EARLIEST	FOREIGN APPLIC	CATION(S), IF ANY, FII THIS APPLICATIO	LED WITHIN 12 MONTHS PRIOR TO ON
Prior Foreign A	application:		
Country	Appl. No.	Filing Date	<u>Priority Claimed</u> <u>Yes/No</u>
ALL FORE	IGN APPLICATIO	ONS, IF ANY, FILED MO THIS APPLICATION	ORE THAN 12 MONTHS PRIOR TO
prosecute this a MORTON J. F ESQ., Reg. 40,	ephone calls to MO respondence to RO 345 EL FA	act all business in the Patent, Reg. 26,049; DAVID I. K	RIVE-SUITE 101



I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further, that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

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